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Title of Invention:	Integrated Circuit and Method for Packet Switching Control  Johannus Theodorus Matheus Dielissen			
First Named Inventor/Applicant Name:				
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_		A_Router_Architecture.pdf _	714652	no	8
3	NPL Docum <b>en</b> ts		cfcad5374fa88e7dfbcfe6e424ef62f813473 3d5		
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4	NPL Documents	Concepts_and_Implementatio n.pdf	575195 0dbfbc3c4206eaf4e5b6e20bc3006b5df10f 2cf7	no	6
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5	NPL Documents	Packetized_On- Chip_Interconnect.pdf	727861	no	6
	IN E Documents		4947f307c90d1ae7805c92ef1c79da149c48 1566		
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6	NPL Documents	Trade_Offs_in_the_Design_of_ a_Router.pdf	707718 aeed2e3b031ecfbb282789d95c917189414	no	6
Warnings:			3df8f		
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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

## New International Application Filed with the USPTO as a Receiving Office

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